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interface means for exchanging data and addresses with an external system;
an address conversion table for converting sector address information input from the external system into a physical block number for identifying a block of the plurality of blocks; and
means for converting the sector address information input from the external system into the physical block number, by referring to the address conversion table, and for accessing the flash memory according to the physical block number.

64. The semiconductor disk device according to claim 63, wherein the sector address information includes a logical sector address.

65. The semiconductor disk device according to claim 64, wherein said address conversion table includes a logical sector address storage section and a physical block number storage section--

REMARKS

The present application, at page 11, lines 27-31, incorporates by reference another application entitled "Multi-State EEprom Read and Write Circuits and Techniques", filed on the same day as the initial parent to the present application, namely April 13, 1989, by Sanjay Mehrotra and Eliyahou Harari, two of the inventors who are also named in the present application. This incorporated application is Serial No. 07/337,579, now abandoned, continuations-in-part of which have issued as patents nos. 5,163,021 and 5,172,338. The present amendment inserts a majority of the incorporated Serial No. 07/337,579 into the present application.

Therefore, essentially all of the Summary of the Invention, Brief Description of the Drawings and Description of the Preferred Embodiments sections of Serial No. 07/337,579 are being added to the present application. In the course of doing so, the patent numbers for referenced applications have also been added. A major revision that has been made to this added text is a change in the drawing figure numbers. Figures 1-17 of Serial No. 07/337,579 are being renumbered herein as figures 9-25, respectively, in order not to use any of the same figure numbers previously used in the present application. Tables 1 and 2 of the incorporated application have also been relabeled as figures 26 and 27, respectively. Further, the reference numbers of the drawings have been changed by adding 1000 to the reference numbers of the figures being incorporated from Serial No. 07,337,579, in order to avoid duplicating the reference numbers already used in the original figures

of the present application. Corresponding changes have been made to the text of Serial No. 07/337,579 that is being inserted into the present application.

The claims being substituted into this application are being copied from patent no. 5,740,396 of Mason, granted April 14, 1998. Newly substituted claims 63, 64 and 65 are exact copies of claims 1, 2 and 3, respectively, of the Mason patent.

Favorable consideration of the present continuation application is solicited.

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Respectfully submitted,

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